

# A Theory of Speculative Computation

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Projet INDES

January 2010 – Parsec meeting

# Speculations: Motivation

- Speculative computation
  - Value prediction
  - Branch prediction
  - Instruction reordering
- Relaxed memory models
  - Write-buffers allow for  $W \rightarrow R$  and  $W \rightarrow W$
  - But not for  $R \rightarrow R$  and  $R \rightarrow W$

IRIW example

initially  $x = y = 0$

$x := 1$		$y := 1$		$!x; (1)$		$!y; (1)$
				$!y (0)$		$!x (0)$

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Speculations could explain these behaviors

# Valid speculations: an intuition

Intuitively valid

(if ! $p$ then () else $q := tt$ )	$\xrightarrow{wr_{q,tt}}$
(if ! $p$ then () else ())	$\xrightarrow{rd_{p,ff}}$
(if $ff$ then () else ())	$\searrow$
()	$\longrightarrow$

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## Intuitively invalid

(if ! $p$ then () else $p := tt$ )	$\xrightarrow{wr_{p,tt}}$
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# Valid speculations: an intuition

Intuitively valid

$$\begin{array}{l} (\text{if } !p \text{ then } () \text{ else } q := tt) \\ (\text{if } !p \text{ then } () \text{ else } ()) \\ (\text{if } ff \text{ then } () \text{ else } ()) \\ () \end{array} \begin{array}{l} \xrightarrow{wr_{q,tt}} \\ \xrightarrow{rd_{p,ff}} \\ \searrow \\ \longrightarrow \end{array}$$

Intuitively invalid

$$\begin{array}{l} (\text{if } !p \text{ then } () \text{ else } p := tt) \\ (\text{if } !p \text{ then } () \text{ else } ()) \\ (\text{if } tt \text{ then } () \text{ else } ()) \\ () \end{array} \begin{array}{l} \xrightarrow{wr_{p,tt}} \\ \xrightarrow{rd_{p,tt}} \\ \checkmark \\ \longrightarrow \end{array}$$

## Validity

We say that a **speculative** computation is *valid* when it is equivalent by permutations [Berry&Levy'79] to a **normal** (sequential) computation.

# Concurrent speculations

Programmability is an issue with parallel speculations (as it is in relaxed memory models)

- Programmability compromise in relaxed memory models for high-level languages:

## Data Race Freeness (DRF)

Programs free of data races in their **interleaving semantics**, expose (only) **sequentially consistent** behaviors in the relaxed semantics.

- Can we find a similar compromise for parallel speculations?

Speculative Data Race Freeness

- ① Operational semantics for speculations (with locks):
  - Speculative evaluation contexts:  
out-of-order execution, branch prediction
  - Value prediction
- ② Validity of speculations
- ③ Programmability: SDRF
- ④ A variation of the language with **memory barriers**



# The language (locks)

$v ::= x \mid \lambda x e \mid tt \mid ff \mid \text{\textcircled{v}}\text{values}$   
 $e ::= v \mid (e_0 e_1) \quad \text{expressions}$   
| (if  $e$  then  $e_0$  else  $e_1$ )  
| (ref  $e$ ) | (!  $e$ ) | ( $e_0 := e_1$ )  
| (thread  $e$ ) | (with  $\ell$  do  $e$ )

$e_0 ; e_1$  stands for  $(\lambda x e_1 e_0)$  whenever  $x$  is not free in  $e_1$

$\mathbf{E} ::= [] \mid \mathbf{E}[\mathbf{F}] \quad \text{evaluation contexts}$   
 $\mathbf{F} = ([ ] e) \mid (v [ ]) \quad \text{frames}$   
| (if  $[ ]$  then  $e_0$  else  $e_1$ )  
| (ref  $[ ]$ ) | (!  $[ ]$ ) | ( $[ ] := e$ ) | ( $v := [ ]$ )  
| (holding  $\ell$  do  $[ ]$ )

# Speculation Contexts

$\Sigma ::= [] \mid \Sigma[\Phi]$  *speculation contexts*

$\Phi ::= \mathbf{F}$  *speculation frames*

|  $(e \ [])$  |  $(\lambda x \ [] \ e)$

|  $(\text{if } e \ \text{then } [] \ \text{else } e_1)$  |  $(\text{if } e \ \text{then } e_0 \ \text{else } [])$

|  $(e := [])$

$(\lambda x \ [] \ e_0)$  can be seen as  $e_0 ; []$

No speculation for (with  $\ell$  do  $e$ )

$r := (!p) ; q := tt$

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$r := \underbrace{(!p)}_{\mathbf{E}(!p)} ; q := tt$

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$(\lambda x \ [] \ e_0)$  can be seen as  $e_0 ; []$

No speculation for (with  $\ell$  do  $e$ )

$r := \underbrace{(!p)}_{\mathbf{E}[(!p)]} ; \overbrace{q := tt}^{\Sigma[(q:=tt)]}$

$(\text{if } \underbrace{(!r)}_{\mathbf{E}[(!r)]} \ \text{then } \overbrace{p := tt}^{\Sigma_0[(p:=tt)]} \ \text{else } \overbrace{q := tt}^{\Sigma_1[(q:=tt)]})$

# Speculative semantics

$$\begin{array}{lcl}
 \Sigma[(\lambda x e v)] & \xrightarrow[\textcircled{\Sigma}]{\beta} & \Sigma[\{x \mapsto v\} e] \\
 \Sigma[(\text{if } tt \text{ then } e_0 \text{ else } e_1)] & \xrightarrow[\textcircled{\Sigma}]{\checkmark} & \Sigma[e_0] \\
 \Sigma[(\text{if } ff \text{ then } e_0 \text{ else } e_1)] & \xrightarrow[\textcircled{\Sigma}]{\searrow} & \Sigma[e_1] \\
 \Sigma[(\text{ref } v)] & \xrightarrow[\textcircled{\Sigma}]{\nu_{p,v}} & \Sigma[p] \\
 \Sigma[(! p)] & \xrightarrow[\textcircled{\Sigma}]{\text{rd}_{p,v}} & \Sigma[v] \\
 \Sigma[(p := v)] & \xrightarrow[\textcircled{\Sigma}]{\text{wr}_{p,v}} & \Sigma[()] \\
 \mathbf{E}[(\text{thread } e)] & \xrightarrow[\textcircled{\mathbf{E}}]{\text{spw}_e} & \mathbf{E}[()] \\
 \Sigma[(\text{with } l \text{ do } e)] & \xrightarrow[\textcircled{\Sigma}]{\mu} & \Sigma[e] \quad l \in [\Sigma] \\
 \Sigma[(\text{with } l \text{ do } e)] & \xrightarrow[\textcircled{\Sigma}]{\tilde{l}} & \Sigma[(\text{holding } l \text{ do } e)] \quad l \notin [\Sigma] \\
 \mathbf{E}[(\text{holding } l \text{ do } v)] & \xrightarrow[\textcircled{\mathbf{E}}]{\tilde{l}} & \mathbf{E}[v]
 \end{array}$$

$x := 1 \mid y := 1 \mid !x; !y \mid !y; !x$

IRIW example (revisited)

# Semantics: IRIW example

$x := 1$	$ $	$y := 1$	$ $	$!x; !y$	$ $	$!y; !x$
				$\downarrow rd_{y,0}$		$\downarrow rd_{x,0}$
$x := 1$	$ $	$y := 1$	$ $	$!x; 0$	$ $	$!y; 0$

IRIW example (revisited)



# Semantics: IRIW example

$x := 1$	$ $	$y := 1$	$ $	$!x; !y$	$ $	$!y; !x$
				$\downarrow rd_{y,0}$		$\downarrow rd_{x,0}$
$x := 1$	$ $	$y := 1$	$ $	$!x; 0$	$ $	$!y; 0$
$\downarrow wr_{x,1}$		$\downarrow wr_{y,1}$				
$()$	$ $	$()$	$ $	$!x; 0$	$ $	$!y; 0$

IRIW example (revisited)

# Semantics: IRIW example

$x := 1$	$y := 1$	$!x; !y$	$!y; !x$
		$\downarrow rd_{y,0}$	$\downarrow rd_{x,0}$
$x := 1$	$y := 1$	$!x; 0$	$!y; 0$
$\downarrow wr_{x,1}$	$\downarrow wr_{y,1}$		
$()$	$()$	$!x; 0$	$!y; 0$
		$\downarrow rd_{x,1}$	$\downarrow rd_{y,1}$
$()$	$()$	$1; 0$	$1; 0$

IRIW example (revisited)

$$\frac{e \xrightarrow[o]{\text{spw}_{e'}} e''}{(S, L, (t, e) \parallel T) \xrightarrow[t, o]{\text{spw}_{e'}} (S, L, (t, e'') \parallel (t', e') \parallel T)} \quad t' \notin \text{dom}(T) \cup \{t\}$$

$$\frac{e \xrightarrow[o]{a} e'}{(S, L, (t, e) \parallel T) \xrightarrow[t, o]{a} (S', L', (t, e') \parallel T)} \quad a \neq \text{spw}_{e''} \ \& \ (*)$$

$$(*) \left\{ \begin{array}{l} a = \text{rd}_{p,v} \Rightarrow v = S(p) \ \& \ S' = S \ \& \ L' = L \\ a = \text{wr}_{p,v} \Rightarrow S' = S[p := v] \ \& \ L' = L \\ a = \overset{\curvearrowright}{\ell} \Rightarrow S' = S \ \& \ \ell \notin L \ \& \ L' = L \cup \{\ell\} \\ a = \overset{\curvearrowleft}{\ell} \Rightarrow S' = S \ \& \ L' = L - \{\ell\} \\ \dots \end{array} \right.$$

# The need for Validity

$$\begin{array}{l} [ \{p \mapsto ff\}, \emptyset, (\text{if } !p \text{ then } p := tt) ] \\ \quad \downarrow wr_{p,tt} \\ [ \{p \mapsto tt\}, \emptyset, (\text{if } !p \text{ then } ()) ] \\ \quad \downarrow rd_{p,tt} \\ [ \{p \mapsto tt\}, \emptyset, (\text{if } tt \text{ then } ()) ] \end{array}$$

Causality is not enforced by the semantics

# The need for Validity

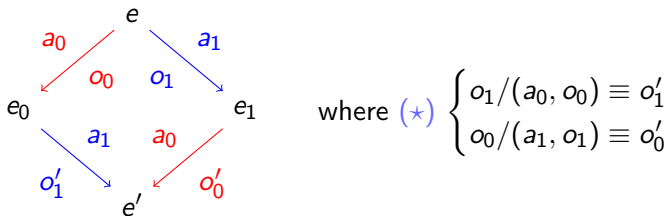
$$\begin{array}{l} [ \{p \mapsto ff\}, \emptyset, (\text{if } !p \text{ then } p := tt) ] \\ \quad \downarrow wr_{p,tt} \\ [ \{p \mapsto tt\}, \emptyset, (\text{if } !p \text{ then } ()) ] \\ \quad \downarrow rd_{p,tt} \\ [ \{p \mapsto tt\}, \emptyset, (\text{if } tt \text{ then } ()) ] \end{array}$$

Causality is not enforced by the semantics

## Validity

We say that a **speculative** computation is *valid* when it is equivalent by permutations [Berry&Levy'79] to a **normal** (sequential) computation.

## Diamond Lemma



- $(*)$  rules out permutations of **control dependent events**:

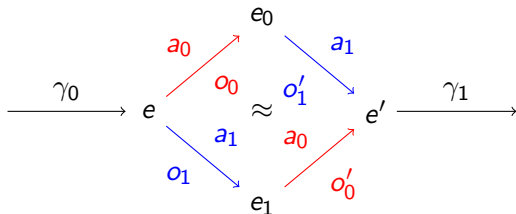
(if  $tt$  then  $()$  else  $p := tt$ )  $\xrightarrow{wr_{p,tt}}$   $\swarrow$   $\rightarrow$   $()$

- What about permutation of **data dependent events**?

$$\# = \bigcup_{p \in \text{Ref}, v, w \in \text{Val}} \{ (wr_{p,v}, wr_{p,w}), (wr_{p,v}, rd_{p,w}), (rd_{p,v}, wr_{p,w}) \}$$

## Equivalence by Permutations

- Given that  $\neg a_0 \# a_1$  we have:



### Valid Speculative Computation

A speculation is valid if it is equivalent by permutation to a normal computation. A speculative computation  $\gamma$  is valid if all its thread projections  $\gamma|_t$  are valid speculations

# Speculatively Data Race Free

- By valid speculations we can explain most of the Java Memory Model litmus tests
- But it fails for DRF programs:

(if ! $p$  then  $q := tt$ ) | (if ! $q$  then  $p := tt$ )

- We need a stronger property:

## DRF Configuration (resp. Speculative DRF Configuration)

A configuration  $C$  is DRF (resp. SDRF) iff for any configuration  $C'$  reachable from  $C$  by normal (resp. **speculative**) computations,

such that  $C' \xrightarrow[t_0, \sigma_0]{a_0} C_0$  and  $C' \xrightarrow[t_1, \sigma_1]{a_1} C_1$  we have

$$t_0 \neq t_1 \Rightarrow \neg(a_0 \# a_1)$$



## Theorem (Main Result)

*Every configuration reachable from a Speculatively Data Race Free closed expressions by a speculative computation is also reachable by a normal computation.*

# A lower level language (barriers)

- Assuming that we have locks is not necessarily realistic for lower level languages
- The DRF (cf. SDRF) guarantee is not very useful for these languages

$$\begin{array}{ll} v ::= x \mid \lambda x e \mid tt \mid ff \mid () & \text{values} \\ e ::= v \mid (e_0 e_1) & \text{expressions} \\ & \mid (\text{if } e \text{ then } e_0 \text{ else } e_1) \\ & \mid (\text{ref } e) \mid (! e) \mid (e_0 := e_1) \mid (\text{spin } e) \\ & \mid (\text{thread } e) \mid rr \mid rw \mid wr \mid ww \end{array}$$

- The dependency relation permutations across barriers

$$\begin{aligned} \bowtie = & \# \bigcup_{p \in \text{Ref}} \{(\text{spw}, \text{rd}_p), (\text{spw}, \text{rd}_p), (\text{spw}, \text{wr}_p), (\text{wr}_p, \text{spw})\} \\ & \cup \bigcup_{p \in \text{Ref}} \{(\text{rd}_p, \text{rr}), (\text{rr}, \text{rd}_p), (\text{wr}, \text{rd}_p), (\text{rd}_p, \text{rw})\} \\ & \cup \bigcup_{p \in \text{Ref}} \{(\text{wr}_p, \text{ww}), (\text{ww}, \text{wr}_p), (\text{rw}, \text{wr}_p), (\text{wr}_p, \text{wr})\} \end{aligned}$$

- Permutation equivalence as before (considering  $\bowtie$ )
- Valid Speculative computation as before

# Preserving Order of Shared Memory Accesses

## POSMA

A configuration  $C$  Preserves Ordering of Shared Memory Accesses (POSMA) iff for any *valid* speculative computation  $\gamma : (C \xrightarrow{*} C')$  with

$$\gamma = \gamma_0 \cdot \frac{a_0}{t, \sigma_0} \rightarrow \cdot \frac{a'_0}{t', \sigma'_0} \rightarrow \cdot \gamma_1 \cdot \frac{a'_1}{t'', \sigma'_1} \rightarrow \cdot \frac{a_1}{t, \sigma_1} \rightarrow \cdot \gamma_2$$

and where  $t' \neq t \neq t''$ ,  $\neg(a_0 \# a_1)$ ,  $a_0 \neq a_1$  and  $a_i \# a'_i$  we have

$$[\gamma_0|_t, (a_0, \sigma_0)] \prec_{\gamma|_t} [\gamma_0|_t \cdot \frac{\sigma_0}{a_0} \rightarrow \cdot \gamma_1, (a_1, \sigma_1)]$$

## Theorem (POSMA Main Result)

*Every configuration reachable from a POSMA well-formed closed configuration by a valid speculative computation is also reachable by a normal computation.*

- How do we make SDRF and POSMA useful for programming?
  - Common data-race detection type systems check for SDRF rather than DRF
  - Enforcement of SDRF by compilation [some work that we did]
    - Type-directed compilation
  - Enforcement of POSMA by compilation [work in progress]
- Prove that common synchronization implementations are POSMA (eg. spinlocks in TSO)